



## AN OPTIMIZED PID PARAMETERS FOR LFC IN INTERCONNECTED POWER SYSTEMS USING MLSL OPTIMIZATION ALGORITHM

Mushtaq Najeeb<sup>1,2</sup>, Mohammed Shahooth, Arrak Mohaisen, Ramdan bin Razali<sup>1</sup> and Hamdan bin Daniyal<sup>1</sup>

<sup>1</sup>Faculty of Electrical and Electronics Engineering, Universiti Malaysia Pahang, Pekan, Pahang, Malaysia

<sup>2</sup>Electrical Engineering Department, College of Engineering, University of Anbar, Ramadi, Iraq

E-Mail: [eng.mushtaq2004@gmail.com](mailto:eng.mushtaq2004@gmail.com)

### ABSTRACT

This research presents the load frequency control (LFC) of three interconnected power systems using a Multi-Level Single Linkage algorithm (MLSL) and a proportional-integral-derivative (PID) control approach. The conventional PID controller is developed using MLSL optimization algorithm including the LFC loop to minimize the frequency deviation and regulate the power exchange because of the load disturbance changes in area1 and area2. In order to enhance the dynamic performance, the optimal parameters of the PID scheme which optimized by the proposed MLSL algorithm are compared with that one's obtained by GA algorithm. Integral Square Error (ISE) is considered as an objective function for both algorithms to determine its performance index value for the same interconnected power system. The results show that the performance of the proposed method is more accurate and faster as well in response to the settling time, maximum deviation, and peak time. The combination algorithms set of MLSL\_PID\_ISE and GA\_PID\_ISE are coded and simulated using MATLAB.

**Keywords:** LFC, MLSL optimization algorithm based PID controller, GA algorithm, ISE.