# Five-Level Single Source Voltage Converter Controlled Using Selective Harmonic Elimination

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## ABSTRACT

The paper presents a 5-level cascaded H-bridge voltage source inverter. The converter topology composed of two-cascaded H-bridge modules connected in parallel and powered by a single DC source. The benefit of this topology in comparison with the conventional H-Bridge configuration is that it uses single input DC source instead of two to achieve the same output steps/levels. Selective Harmonic Elimination (SHE) is the modulation technique employed. The generated non-linear transcendental equations are solved using an optimised Genetic algorithms to find the switching angles. This property makes the topology and the control function suitable for three phase applications where triplen harmonics are said to cancel out at the line-to-line voltages. This concept of SHE modulation extends the value of the filter cut-off frequency, which translates to smaller sized filter, compact cooling system and reduced system weight. This advantage makes the topology attractive to automotive and renewable energy applications. The topology was simulated using PSIM software.

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## 1. INTRODUCTION

A Multi-level Inverter (MLI) can merely be described as an electrical device comprising of power electronic switches arranged and controlled systematically, such that it converts a Dc input source coming from either photovoltaic cells, batteries, capacitors etc. into multiple stepped output voltage waveform close to a sinusoidal signal. Multilevel inverter popularity is due to the various advantages it offered over the conventional square wave inverter. The converter modular nature coupled with the advances made in semiconductor technology has broadened up its usage in high power industrial applications. Moreover, it provides less switching stress on power switches, therefore reducing switching losses in the circuit which improves the system efficiency. It reduced output total harmonic distortion (THD), and electromagnetic interference (EMI) makes it suitable for highly sensitive applications [1]-[3].

Baker's in the mid 70's proposed Cascaded H-bridge (CHB) topology which he published in a patent titled 'Electric power converters' [4]. It was the first inverter topology to generates multiple output steps using more than one Dc source. Later in the 80's, they proposed another topology which is popularly known as Neutral point clamped converter (NPC) or Diode Clamped topology. The benefit of this topology over the earlier one is it uses single DC source with additional diodes and capacitors to achieve higher output voltage steps. In the same year, Nabae et al., applies the Pulse Width Modulation (PWM) control scheme on the NPC topology that produces an output with less harmonic distortion [5]. Most of the research done in the 80's were

more focused towards three level converter topologies, but later in the 1990s, the number of steps was increased up to six levels [6].

In the 90's, a new topology was proposed by Meynard and Foch, which was called Flying Capacitor (FC) [7]. It circuit connection is similar to that of a Diode Clamp, only that the diodes were replaced with capacitors. One of the benefits of flying capacitor over diode clamp is its redundancy property. It can generate a particular output voltage using multiple switching sequences [8]. Most of the early literature was based on the above three mentioned topologies, which makes them be termed as classical topologies.

Recently, some new topologies were developed, most of which are application base driven. This is because a particular topology tends to be more effective and efficient in a specific application and unsuitable in another [9], [10]. Hence, making both academia and industry to explore more topologies that suits a particular application.

The positive impact MLI's are making at domestic and industrial level attracts more research interest which focuses on improving its topologies and control scheme. One major MLI limitation is the relationship between the number of components and the voltage output steps, which is said to be in a direct proportion to each other. Therefore, the circuit tends to be larger in size, complicated and more expensive. To overcome this limitation, researchers and engineers are developing new topologies with reduced device count [11], [12].

This paper is equally part of the same research, aiming at generating more output voltage step with fewer components. The article uses two modules of the H-bridge inverter, connected in parallel and both powered using a single DC source. The source could be either battery, photovoltaic cell or fuel cell. The two modules are controlled using switching angles obtained by solving the non-linear transcendental equations generated through SHE-PWM theorem, which eliminates N-1 harmonics, with N been the number of switching angles. Various techniques exist for solving such non-linear equations that has several solutions due to their trigonometric nature [13], [14]. However, in this paper, an optimised genetic algorithm technique proposed in [15] was used to get the optimal solutions of the switching angles. The selective property of SHE-technique is employed where some selected lower order odd non-triplen harmonics were eliminated. Which further increase the output filter cut off frequency, hence reducing the system filter size. If the set up is transformed into three phase, it is expected that the output Triplen harmonics to cancel out in the line-to-line voltage. Therefore, this paper presents a single-phase five level converter controlled using three SHE-PWM control scheme. The study demonstrates the efficacy of the proposed topology and the G.A optimisation technique used in finding the optimal switching angles.

#### 2. MATERIALS AND METHODS

#### 2.1. Proposed converter topology and circuit operations

Figure 1 shows the converter topology used to achieve the five-level output waveform. The circuit comprises of two H-bridge modules connected in parallel and both supplied by a single Dc source. The source could either be a photovoltaic cell, rectified wind energy, fuel cell or lithium-ion batteries. The two modules comprise of eight power electronic switches, with each having four switches. Each module is capable of generating three output steps (+E, 0, -E). The secondary transformer terminals are connected in series with each other and their respective voltages VP2 and VP3 are said to add up across the load. With proper switching function, the converter is capable of generating five level output waveform. The topology also offers additional switching redundant state, which could be used in fault situations.



Figure 1. Proposed topology inverter configuration

To synthesize the voltage waveform at the terminals, the gating signal of each IGBT needs to be generated based on the calculated switching angles. The IGBT's conduction states during the positive and negative half cycle in both H-bridges are given in Table 1. However, other redundant switching states can as well be explored to address switching stress and failure rate of the switching devices. It is worth noting that switches on the same leg are operated complementarily to avoid short-circuiting the dc source.

Table 1. Selected switching combinations

Voltage	Swit	Switching states							
	S <sub>1</sub>	$S_2$	S <sub>3</sub>	$S_4$	$S_5$	$S_6$	S <sub>7</sub>	S <sub>8</sub>	
0	1	0	1	0	1	0	1	0	
+E	1	0	0	1	1	0	0	1	
0	0	1	0	1	0	1	0	1	
-E	0	1	1	0	0	1	1	0	

## 2.2. Five level 3/9 Output voltage waveform design and switching angle calculation

The modulation technique used in any converter topology played a significant role in the obtained output waveform [16]. In this case, selective harmonic elimination technique (SHE) is the method to be used to generate the five-level output voltage with 3/9 angle distribution ratio. While trying to achieve the said output level, the power electronic switches are controlled such that the targeted selected harmonics are successfully eliminated. SHE apart from reducing THD in the output, it also minimises electromagnetic interference (EMI) and switching losses in the system [17], [18].

To generate the waveform with 3/9 switching distribution ratio, a rough sketch of the five-level waveform is initially made using arbitrary values of switching angles. Figure 2 depicts a rough sketch of the targeted waveform with the assumption that it obeys the quarter-wave symmetry theorem with twelve switching angles ( $\alpha_1$  to  $\alpha_{12}$ ) per quarter cycle [18].



Figure 2. Five Levels waveform with 3/9 distribution ratio

Based on quarter wave theorem, sinusoidal signal results in DC and even harmonic components cancellation, the output components contain only the fundamental and odd harmonics [19]. To eliminate individual harmonics, the fundamental component needs to be systemically controlled during the positive and negative half cycles at specific angles. With this operation, the selected lower order (N-1) non-triple harmonics are eliminated, where N is the number of switching angles in a quarter wave [14], [17].

Therefore, the following harmonics  $5^{th}$ ,  $7^{th}$ ,  $11^{th}$ ,  $13^{th}$ ,  $17^{th}$ ,  $19^{th}$ ,  $23^{rd}$ ,  $25^{th}$ ,  $29^{th}$ ,  $31^{st}$ ,  $35^{th}$  order are eliminated in the output. This further shifts the output filter cut off frequecy, hence reducing the system filter size. If the topology is transformed to three phase, it is expected that the output triplen harmonics will cancel out in the line-to-line voltage.

The Fourier series expansion of the five-level waveform in Figure 2 is generated using the expressions [20].

$$V_{out}(wt) = \sum_{n=1}^{\infty} a_n \, \sin(nwt) \tag{1}$$

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Where

$$a_n = \frac{4E}{n\pi} \sum_{K=1}^{N} (-1)^{K+1} \cos(n\alpha_K)$$
(2)

for odd values of n

$$b_n = 0$$
 (3) for all value of n

 $\alpha_{\rm K}$  Denotes the switching angles and most satisfies the relationship,

$$\alpha_1 < \alpha_2 < \dots < \alpha_N < \frac{\pi}{2} \tag{4}$$

E is the input dc supply voltage, N is the switching angles per quarter wave, n is the odd harmonic order, w is the angular frequency, t is time.

Simplifying Equation (1) and (2) The amplitude of the fundamental and non-triplen odd harmonic components in the five-level output waveform is represented by the non-linear transcendental equations given in Equation (5) and (6):

$$h_1 = \frac{4*E}{\pi} \{ \cos(\alpha_1) - \cos(\alpha_2) + \dots \pm \cos(\alpha_{12}) \}$$
(5)

$$h_1 = \frac{4 * E}{n\pi} \{ \cos(n\alpha_1) - \cos(n\alpha_2) + \dots \pm \cos(n\alpha_{12}) \}$$
(6)

The amplitude of the fundamental component is controlled by the modulation index (M) and is given by the expression.

Modulation Index (M) = 
$$\frac{h_1}{E}$$
 (7)  
For all n = 1  
While M=0 for all n + 1

Hence, using Equation (5), (6) and (7) the non-linear transcendental equations can be expressed as:

$$\cos(\alpha_1) - \cos(\alpha_2) + \cos(\alpha_3) - \cos(\alpha_{11}) - \cos(\alpha_{12}) = \frac{M\pi}{4}$$
(8)

$$\cos(5\alpha_1) - \cos(5\alpha_2) + \cos(5\alpha_3) - \ldots + \cos(5\alpha_{11}) - \cos(5\alpha_{12}) = 0$$
(9)

$$\cos(35\alpha_1) - \cos(35\alpha_2) + \cos(35\alpha_3) - .. + \cos(35\alpha_{11}) - \cos(35\alpha_{12}) = 0$$
(10)

The above transcendental equations are solved to find the approximate solution of the switching angles, capable of eliminating the selected lower order harmonics. Given the nonlinearity and the trigonometric content of the equations, finding the solution using standard mathematical solution is not possible because multiple solutions exist. Therefore, to get the optimal values, a hybrid genetic algorithm presented in [15] was used to find the solution. The technique uses different values of overmodulation index (M) to find the solutions for the switching angles  $\alpha_1, \alpha_2, ..., \alpha_{12}$ .

## 2.3. Module terminal voltages design and gating signals generation

Figures 3a and 3b are the expected waveforms at the terminals of module 1 and 2 with their corresponding gating signals. The terminal voltages add up to generate the five-level output voltage across the load. Both the terminal and gating signals were generated based on the switching combinations presented in Table 1. As seen, the majority of the notches comes from the first module. However, the research work is not after analysing the power-sharing and switching stress in the system. Its primary target is to employ SHE-modulation technique using less number of devices/components to achieve higher-level output with less output THD. Therefore, balancing the switching stress and power transfer is outside the scope of this paper.

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Figure 3. Designed converter terminal voltages

#### **RESULTS AND DISCUSSION** 3

#### 3.1. Switching Angles Solutions

The generated equations as stated earlier were solved using an optimised genetic algorithm with M= 3.2. The solution provides 12 switching angles as shown in Table 2. The remaining switching angles were obtained using quarter wave symmetry. These angles are directly fed into the PSIM software pulse generators to produce a 50Hz gating signal. With the appropriate gate pulse, terminal voltages and gating signals similar to the ones in Figure 3a and 3b are generated. The terminal voltages add up across the load to produce a five-level output waveform with 3/9 distribution ratio. Depending on the harmonic coefficients used in the transcendental equation, 11 selected harmonics are eliminated from the converter output.

The computed angles are converted into the time domain, which is then programmed using C on a microcontroller to generate the pulsating signals.

Table 2. Five level	Voltage level 3/9 M <sub>I</sub> =	3.2
s/n	0-90	
$\alpha_n$		
1	13.40246	
2	15.67567	
3	19.61681	
4	35.50001	
5	37.80673	
6	46.26136	
7	48.24797	
8	54.85481	
9	58.37752	
10	61.01313	
11	83.87128	
12	86.01930	

#### 3.2. Simulation Results

The proposed topology modular terminal voltage waveforms VP2 and VP3, together with their corresponding IGBT control signals, are shown in Figure 4a and 4b, respectively. Both voltage waveforms look the same with the pre-sketched pattern presented in the methodology section. The gating signals for the eight IGBT's are switched in conformity with the computed angles and the voltage switching combination given in Table 1.

Figure 5a shows the five-level fundamental output voltage waveform across a resistive load. The fundamental voltage is chopped several times at a pre-calculated switching point, satisfying the 3/9 distribution ratio as sketched in Figure 2. Note that the output voltage across the load is expressed as the sum of both transformer secondary side voltages VP1 = VP2 + VP3). Figure 5b depicts the output waveform along with a superimposed sinusoidal waveform and the output current in the lower plot. Due to high harmonic content, both voltage and current does not closely follow the pure sinewave pattern.



(a) Module1 (VP2) with gating signal

(b) Module2 (VP3) with gating signal







Figure 6 shows the Fast Fourier transform (FFT) of the proposed five-level over the range of 5 KHz at light loading. From the spectrum, SHE-PWM successfully eliminates all the 11 selected lower order odd non-triplen harmonics that lies within 1.75 KHz (35th) range, leaving behind the triplens (3rd, 9th, 15th, 21st, 27th, 33rd). The first dominant non-triplen harmonic visible is the 37th (1.85 KHz), and the spectrum becomes full of harmonics afterwards, hence, complying with the control function employed.



Figure 6. FFT Spectrum of the five-level waveform

However, since the harmonics are at a higher frequency, a small-sized low pass filter can be used to eliminate them. To demonstrate this, an LC filter is designed for the circuit. Figure 7a shows the filtered output voltage waveform and the current at full load. Both waves look closer to a pure sinewave than the unfiltered wave presented in Figure 5b. Figure 7b is the FFT of the filtered waveform; it appears to have less

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harmonic contents with THD of 4.65% compared with the unfiltered FFT in Figure 6. Hence, the topology is said to satisfy the IEEE THD standard requirement of less than 5%.



Figure 7. Proposed topology

This control technique is suitable for 3-phase applications, where the triplen harmonics are said to cancel each other in the line voltages.

#### 3.3. Comparison between Conventional and Proposed topology

The conventional H-bridge topology and the proposed has the same number of switching components; they differ in the number of input Dc sources and the switching function involved. In conventional, the number of input sources increases with increase in output levels, whereas, the proposed topology uses single Dc source irrespective of the number of output levels. Figure 8a is the conventional output voltage and current waveforms, while 8b is the output voltage FFT with THD of 9.46%.

The THD of the proposed (4.65%) at the same output power is less than that of the conventional (9.46%), and it can even be verifiable by physical assessment of both waveforms of Figures 7 and 8.



Figure 8. Conventional topology

Usually, total harmonic distortion (THD) of an inverter decreases with increase in output power [18]. Figure 9a represents the THD vs output power curves of both the two topologies. The axis to the left represents that of the proposed topology whereas the right axis is for the conventional topology. From the graph, both topologies exhibit the fundamental property of decreasing THD with an increase in output power.

Figure 9b shows the efficiency vs output power plots for the two topologies. Both curves conform to the standard inverter efficiency curve that initially raises from zero to a high value before it then stabilises as the output load increases. The proposed topology has better efficiency than the conventional at all loading

level. The average efficiency value for the conventional and proposed stands at 49.2% and 99.1% respectively. The difference translates to over 100% improvement in efficiency with respect to the conventional. This further ascertains the viability of the proposed topology and the SHE modulation technique employed.



#### 4. CONCLUSIONS

A five-level multilevel converter using cascaded H-bridge topology with a single DC input source and multi-winding transformer was designed, simulated, and validated. The reported converter addressed the issue of converter size by using single Dc source to synthesise a five-level output voltage waveforms, hence eliminating the need for multiple sources. Based on the result obtained, it shows SHE-PWM has the capability of eliminating selected harmonics by adding some notches to the fundamental component at a specific pre-determined angle. The control function used targets odd non-triple lower harmonics, which makes it suitable for three phase applications where triplens cancels out in the line voltage. In addition to that, the converter is compact, has less switching stress and heat dissipation caused by harmonics. This results in a reduction in cooling requirements. Thus, translates to lesser size, weight, production and maintenance cost.

The proposed topology has satisfied the below 5% THD level, which is one of the reason it has high efficiency. All the above features make the converter suitable and attractive to applications where reliability and efficiency are of uttermost importance.

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