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JUDUL: MC68000 MICROPROCESSOR STAND ALONE
EDUCATIONAL BOARD

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MC68000 MICROPROCESSOR STAND ALONE EDUCATIONAL BOARD

HENG POH XUAN

This thesis is submitted as partial fulfillment of the requirements for the award of the
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LIST OF ABBREVIATIONS

CSIC	Complex Instruction Set Computers
LED	Light Emitted Diode
LCD	Liquid Crystal Display
DIP	Dual-In-Line Package
I/O	Input and Output
MACSS	Motorola Advanced Computer System on Silicon
CPU	Central Processing Unit
RAM	Random Access Memory
ROM	Read Only Memory
DC	Direct Current
UMP	University Malaysia Pahang
SCL	Student Central Learning
EBS	Educational Board System
PC	Personal Computer
ARM	Advanced RISC Machine
FPGA	Field Programmable Gate Array
JTAG	Joint Test Action Group
IDE	Integrated Development Environment
UDM	University Detroit Mercy
EVB	Evaluation Board
SoC	System on Chip
SRAM	Static Random Access Memory
SDRAM	Synchronous Dynamic Random Access Memory
VHDL	VHSIC Hardware Description Language

A/D	Analog to Digital
D/A	Digital to Analog
CLK	Clock
TTL	Transistor-Transistor Logic
AS*	Address Strobe
R/W*	Read/Write
UDS*	Upper Data Strobe
LDS*	Lower Data Strobe
SPST	Single-Pole Single-Throw
EEPROM	Electrical Erasable Programmable Read Only Memory
PCB	Printed Circuit Board
SCL	Student Centre Learning
PWM	Pulse Width Modulation

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